

Given a memory of 2048 bytes consisting of several 64x8 RAM chips, and assuming byte addressable memory, which of the following seven diagrams indicates the correct way to use the address bits? Explain your answer.

- a.

← 10-bit address →	
2 bits for chip select	8 bits for address on chip
- b.

← 64-bit address →	
16 bits for chip select	48 bits for address on chip
- c.

← 11-bit address →	
6 bits for chip select	5 bits for address on chip
- d.

← 6-bit address →	
1 bit for chip select	5 bits for address on chip
- e.

← 11-bit address →	
5 bits for chip select	6 bits for address on chip
- f.

← 10-bit address →	
4 bits for chip select	6 bits for address on chip
- g.

← 64-bit address →	
8 bits for chip select	56 bits for address on chip