1. With reference to the block diagram of FIGURE 1, state the two conditions that must be satisfied to give an oscillatory output.

*si so*



*G*

*si – Hso*

*–*

*Hso*

*H*

FIG. 1

1. With reference to the block diagram of FIGURE 1, determine the required value of *G* to give an oscillatory output if *H* = –10 dB.
2. FIGURE 2 shows a public address system.
   1. It is found that if the microphone is brought into proximity of the loudspeaker, the systems will ‘howl’. Carefully explain, making reference to feedback theory why this is so.
   2. Suggest two actions that could be adopted to remedy the ‘howling’.
   3. Measurements show that for a particular arrangement of the equipment and at a particular amplifier setting, the system will howl if 1% of the output power is fed back to the microphone. Estimate the power gain of the P.A. amplifier in decibels.

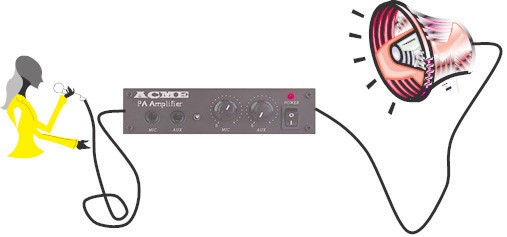


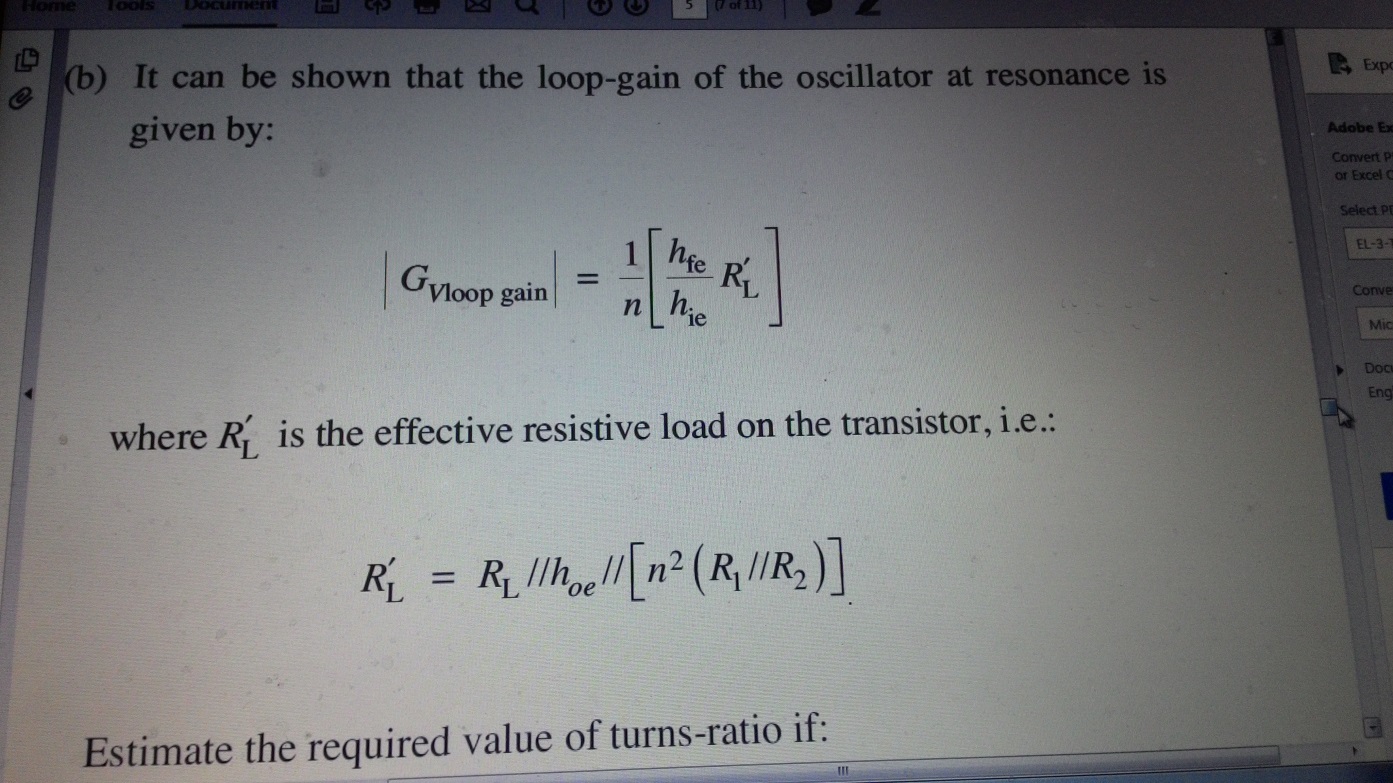
FIG. 2

1. FIGURE 3(a) shows the circuit of an Armstrong oscillator (named after its inventor, the American engineer Edwin Armstrong in 1912). Here a transformer is used to couple the output to the input to give feedback. The transformer has a turns ratio of *n*:1, where *n* represents the primary winding.

In this particular circuit the transistor’s emitter resistor is bypassed by a large capacitor at a.c. frequencies and its base is biased via the transformer windings.

FIGURE 3(b) represents the a.c. equivalent circuit of the oscillator and (c) its *h*-parameter equivalent circuit.

* 1. Explain the significance of the transformer’s dot notation in relation to the operation of the oscillator.
  2. It can be shown that the loop-gain of the oscillator at resonance is given by:



where *R* is the effective resistive load on the transistor, i.e.:

L

*R*L 



*R*L //*hoe* // ⎡⎣*n*2

*R*1 //*R*2 ⎤⎦

Estimate the required value of turns-ratio if:

*R*1 = 4.7 k, *R*2 = 24 k, *R*L = 2.7 k, *h*fe = 250, *h*oe = 10–5 S, *h*ie = 4 k

*C1*



*+VCC*

*R*

*2*

*L*

*CL RL*

*1:n*

*Vo*

*Vi*

*R1*

*RE*

*CE*



*1:n*

*V*

*Vo*

*i*

(b)

(a)

FIGS. 3(a) and 3(b)

*RL CL*



*R1//R2*

*Cin*

*ib*

*hie*

*n:1*

*Vin*

*hoe*

*Co*

*Vo*

*L*

*hfe i*

*b*

(c)

FIG. 3(c)

1. FIGURE 4 shows another variation the Armstrong oscillator. A transformer with two secondary windings has been use, one to give feedback and one to give the oscillator’s output.

Write a short report [two to three pages] on an investigation into the operation and performance of this circuit1.

The report should embrace, as far as you are able, the following themes:

* 1. Why the output is taken via the transformer rather than directly off the collector of the transistor.
  2. The agreement between the measured and calculated quiescent voltages on the three terminals of the transistor.

1The circuit model is available in the module’s Learning Materials on Blackboard. In this simulation two extra components, C5/R5, have been added to the left of C2. The added capacitor carries a small initial voltage to act as the necessary noise required to ‘kick start’ the oscillator. The default run time is from 100 to 102 milliseconds. The transformer has been formed from three mutually coupled inductors, rather than using the transformer model. This has been done because the parameter ‘inductance’ can be swept for an inductor in an

a.c. analysis. This facility is not available in the transformer model.

* 1. The agreement between the measured and calculated frequency of oscillation.
  2. The shape of the output waveform in the first 10 milliseconds of start-up.
  3. The given L1:L2 ratio is not necessarily the optimum value to give a good sinusoidal output. [A Fourier probe on the output will give a spectral response]. Try to devise an experiment to find the optimum ratio\*. Express the ratio as a *turns* ratio.

The report should include copies of any graphical responses produced in the investigation.

\*This can be done by performing an AC sweep on the inductance parameter L2. However a sweep cannot be performed without a voltage source in the circuit. For the purposes of this analysis a small voltage source can be inserted into the feedback loop as shown in the second version of the circuit on Blackboard.

*5*



*18k*

*R3*

*2.7k*

*R1*

*10u*

*Probe2-NODE*

*Q2*

*BC 109*

*C3*

*10m*

*L3*

*C2*

*1u*

*C*

*4*

*1*



*L1*

*10m*

*8.2k*

*R4*

*1k*

*R2*

*C1*

*10u*

*1m*

*L2*

*V1*

FIG. 4

|  |  |
| --- | --- |
| **Question No.** |  |
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