



FIGURE 7-33

4. Study the ladder logic program in Figure 7-33, and answer the questions that follow:
- What type of timer has been programmed?
 - What is the length of the time-delay period?
 - When does the timer start timing?
 - When is the timer reset?
 - When will rung 3 be true?
 - When will rung 5 be true?
 - When will output PL4 be energized?
 - Assume that your accumulated time value is up to 020 and power to your system is lost. What will your accumulated time value be when power is restored?
 - What happens if inputs PB1 and PB2 are both true at the same time?