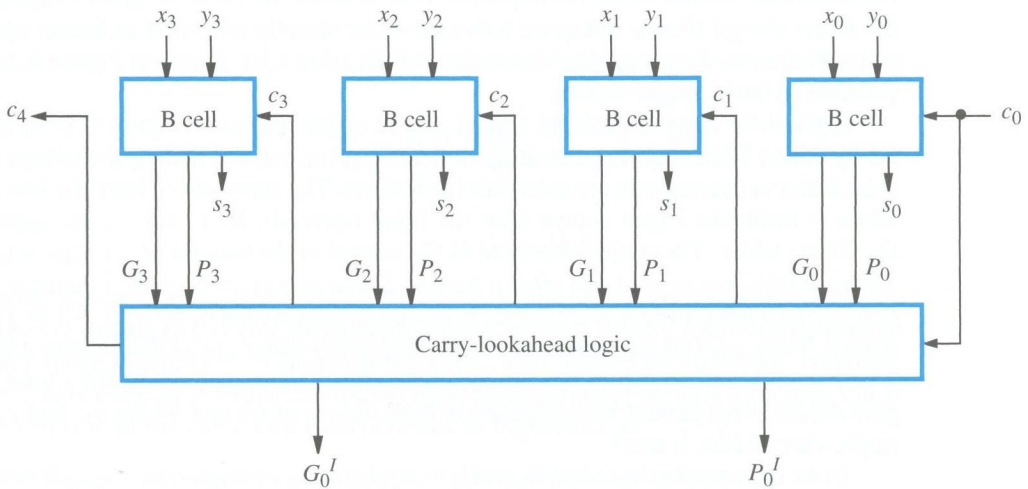


(a) Bit-stage cell



(b) 4-bit adder

**Figure 6.4** 4-bit carry-lookahead adder.